

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
DAISUKE MIURA, et al.) : Examiner: Julio J. Maldonado
Application No.: 10/555,614) : Group Art Unit: 2823
371(c) Date: November 4, 2005) :
For: FIELD EFFECT TRANSISTOR,)
METHOD OF PRODUCING :
THE SAME, AND METHOD)
OF PRODUCING LAMINATED :
MEMBER) : May 27, 2008 (first business day
: (following Memorial Day holiday)

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action dated February 26, 2008, please amend the above-identified application, as follows: